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PATENT, TRADEMARK AND COPYRIGHT LAW

FACSIMILE: (703) 684-1157



Date: March 7, 2002

Attorney Docket No. H-1030



To: Assistant Commissioner for Patents

Washington, D.C. 20231

Sir: Transmitted herewith for filing is the patent application of:

Inventor: K. ENDO et al (see attached)

SEMICONDUCTOR INTEGRATED CIRCUIT WITH VOLTAGE GENERATION CIRCUIT, LIQUID CRYSTAL DISPLAY CONTROLLER

AND MOBILE ELECTRIC EQUIPMENT Enclosed are:

Lilciosca d	
X	Sheets of Drawings
X	This application is being filed without an executed Declaration.
	Priority is claimed from Japanese Application No. 2001-110813
X	filed April 10, 2001 .
X	Copies of the disclosure documents listed on the attached PTO 1449 form and discussed in the specification or attached Information Disclosure Statement.
	A verified statement to establish small entity status under 37 CFR 1.9 and 1.27.
X	Specification: Abstract X , Description 44 pages; and 14 claim(s).
X	Preliminary Amendment.
	Executed Declaration.

The filing fee is calculated as shown below:

For:	No. Filed	No. Extra		
Basic Fee				
Total Claims	14 -20=	* 0		
Indep Claims	3 - 3 =	* 0		
Multiple Dependent Claim (s)				

If difference is less than zero then enter 'O' in second column

X

Rate	Fee
	\$ 370
x 9	\$
x 42	\$
+ 140	\$

\$

Small Entity

OR

Rate	Fee
	\$ 740
x 18	\$ 0
x 84	\$ 0
+ 280	\$ 0
Total	\$ 740

Large Entity

X	A check in the amount of \$	740.00	is enclosed for the filing fee.
∇	The Commissioner is hereby auth	orized to charge any	additional fees that may be required to

Deposit Account No. 50-1417.

Total

Respectfully Submitted,

nn R. Mattingly

Registration No.

LIST OF INVENTORS' NAMES AND ADDRESSES

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